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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE



Applicant: K. KATSURA, et al.

GROUP 240

Serial No.: 07/985,141

Filing Date: December 3, 1992

For: GRAPHIC PROCESSING APPARATUS
UTILIZING IMPROVED DATA TRANSFER
TO REDUCE MEMORY SIZE

Group: 2301

Examiner: M. Zimmerman

~~ENTER~~

AMENDMENT

NOT ENTERED

Honorable Commissioner of Patents
and Trademarks
Washington, DC 20231

August 17, 1995

Sir:

The following amendments and remarks are being
submitted in response to the Office Action dated February
17, 1995.

IN THE CLAIMS:

Please amend the claims as follows:

Claim 1, line 22, after "storing" insert -- successive
groups of m bits of --

lines 22-23, delete "received serially on
said memory data bus" and insert -- read out from said
memory means via said memory data bus by performing plural
read operations within a memory cycle --;

line 24, delete "the" and insert -- said --;

line 25, before "row" (first occurrence)
insert -- at a -- also after "row address" insert -- forming